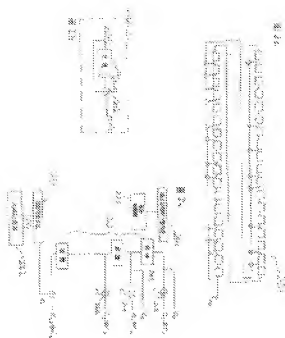


## ■ Biblio+Drawings

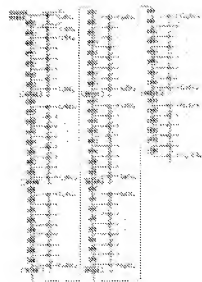
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**Patent/Publication** 318983**Number****Issued/Publication** 1997/11/01**Date****Application Date** 1996/12/23**Application** 085115906**Number****Certification** 090280**Number****IPC** H04J-013/00**Inventor** MICHAEL LUDDY US ;  
ALEXANDER MARRY US**Applicant** INTERDIGITAL  
TECHNOLOGY CORPUS**Priority Number** 19950630 US 60000775  
19960627 US 08669775**Individual** F**Patent Right  
Change**

Application Number	085115906
Date of Update	
Granting of a license	NO
Registration of patent mortgage	NO
Transfer of patent right	NO
Succession of patent right	NO
Registration of patent trust	NO
Opposition filed	NO
Request for invalidation filed	NO
Date of lapse	
Patent revoked	
Date of grant	19971101
Scheduled expiry date	20161222
Due date of annual fee	20101031
Years of annuities paid	013



3333



24



圖 1 系統架構

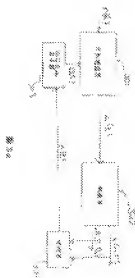


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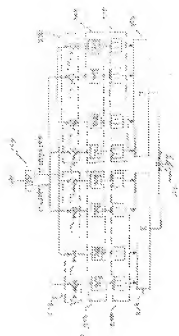
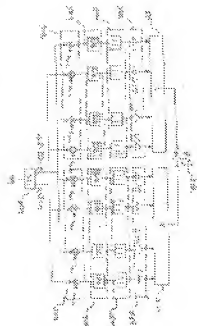


圖 3



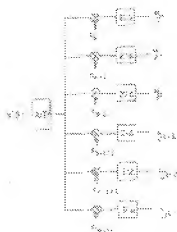
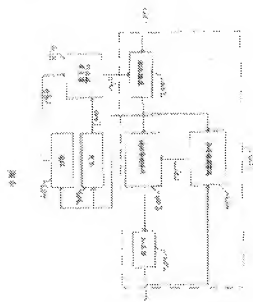


圖 54

圖 55



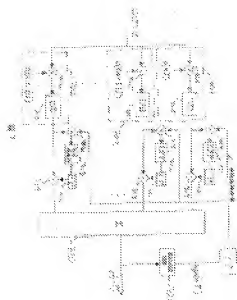
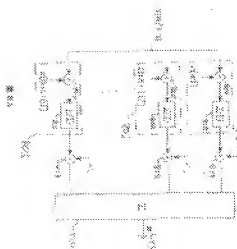
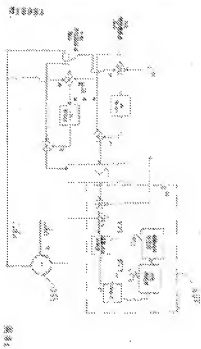
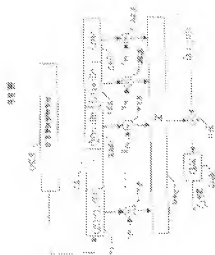


Figure 1

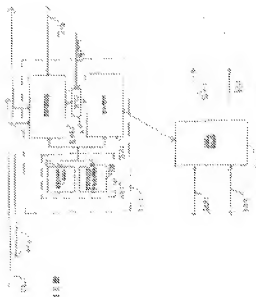
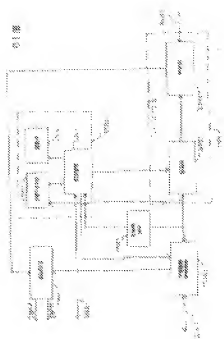








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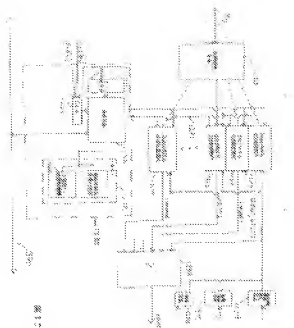


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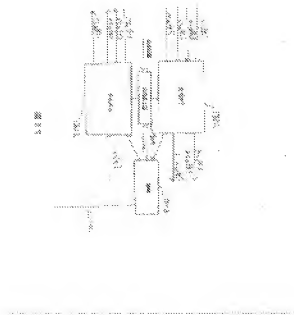


圖 2

圖 14

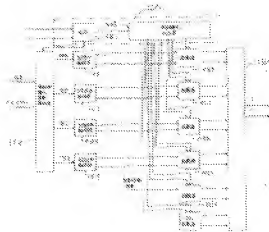


圖 14

圖 15

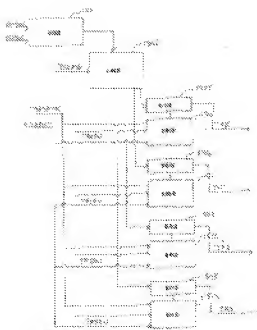


圖 15

電圧調整回路

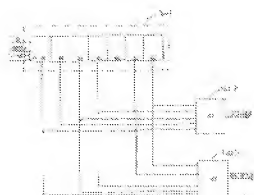


図 1A

電圧調整回路

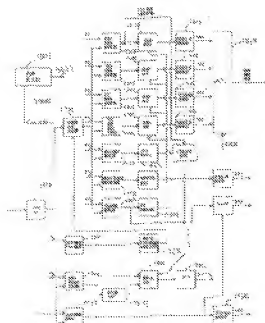


図 1B



圖 2

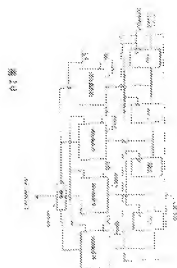


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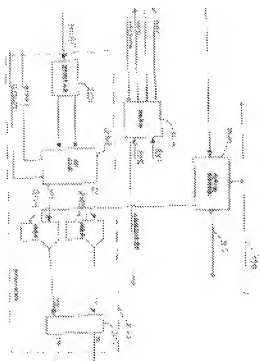


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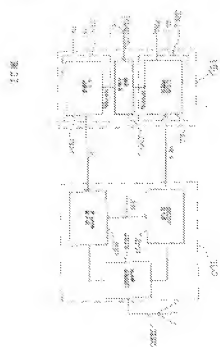
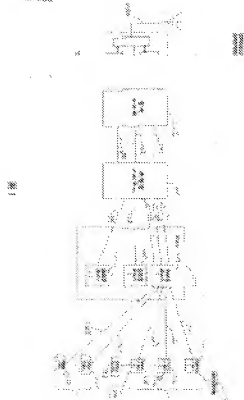


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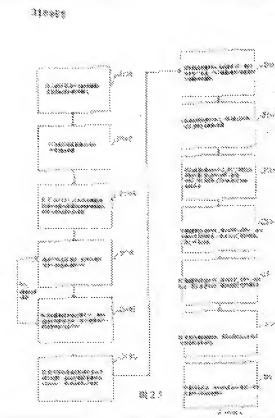
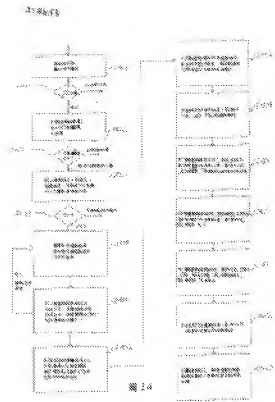




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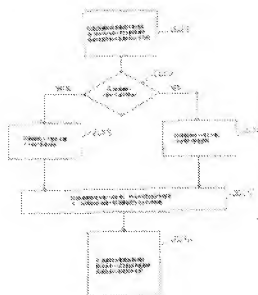


圖 25

圖 26

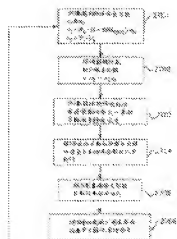


圖 26

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圖 20

A71004

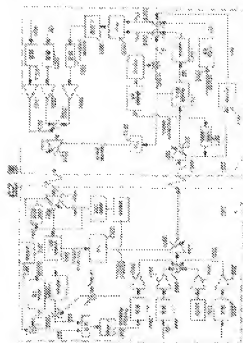


Fig. 10

